

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,225,098 B2  
APPLICATION NO. : 10/535064  
DATED : May 29, 2007  
INVENTOR(S) : Xavier Robert

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

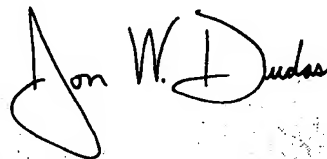
Col. 1, line 62, should read:  
Thus, standard IEEE-ISTO-5001, in preparation, pro-"

Column 2, line 39, should read:  
toring circuit according to standard IEEE-ISTO-5001 which

Column 6, lines 53-56 should read:  
type (ADDR). Block A can then be divided into a sub-block A' intended to only store data of I-CNT type, and a sub-block A" intended to only store addresses ADDR. The boxes of sub-blocks A' and A" are provided each to only respectively store data I-CNT or an address ADDR. Sub-block A' will

Signed and Sealed this

Twenty-fourth Day of July, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas", is written over a faint, rectangular official stamp.

JON W. DUDAS

*Director of the United States Patent and Trademark Office*